

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in this application.

**Listing of Claims:**

Claim 1 (Currently Amended):       An image display apparatus comprising:  
a first function processing system means for processing a first function which is being  
continuously set into an on state;  
a second function processing system means for processing a second function which is  
selectively being set into an on state ~~in case of necessity~~;  
first writing means for writing ~~first~~ image data relating to the said first function to a first  
memory;  
second writing means for writing ~~second~~ image data relating to the said first function to  
~~into~~ a second memory;  
third writing means for writing ~~second~~ image data relating to the said second function to  
the into said second memory;  
display circuitry means for displaying a composite image on a display on the basis of the  
~~first~~ image data stored in the said first memory and the ~~second~~ image data stored in the said  
second memory; and  
~~enabling validating~~ means for selectively enabling the validating said second writing  
means or the said third writing means depending on whether in response to turning on/off the  
second function is turned on or off.

Claim 2 (Currently Amended):       An image display apparatus according to claim 1,  
wherein the said first image data written to the first memory comprises ~~is~~ image data in which  
each dot has a first number of bits, and the said second image data written to the second memory  
comprises ~~is~~ image data in which each dot has a second number of bits more than the said first  
number of bits.

Claim 3 (Currently Amended): An image display apparatus according to claim 1, wherein the said first function is a phone function, the said second function is a game function, the said first image data written to the first memory includes at least character data indicative of a receiving state, the said second image data written to the second memory by the said second writing means includes predetermined image data, and the said second image data written to the second memory by the said third writing means includes game image data.

Claim 4 (Currently Amended): An image display apparatus according to claim 3, wherein the said first function processing system means includes a detector detecting means for detecting an incoming call, the said first writing means includes incoming call message writing means for writing said first image data indicative of an incoming call message to the said first memory when the said incoming call is detected, and the said display circuitry means includes tone modifying means for modifying a tone of the said second image data in the second memory when the said incoming call is detected.

Claim 5 (Currently Amended): An image display apparatus according to claim 1, wherein the said display circuitry means includes fetching means for fetching compositing position information indicative of a compositing position of the said second image data written to the second memory and compositing means for generating composite image data on the basis of the said compositing position information, the said first image data written to the first memory and the said second image data written to the second memory.

Claim 6 (Currently Amended): An image display apparatus according to claim 5, wherein the said first image data written to the first memory is binary image data in which each dot is formed by one bit, the said second image data written to the second memory is color image data in which each dot is formed by a plurality of ~~number of~~ bits, and the said compositing means includes first single color fetching means for fetching first single color data in correspondence to a first predetermined bit value of the said binary image data, second single color fetching means for fetching second single color data in correspondence to a second predetermined bit value of the said binary image data, first selecting means for selecting any one of the said first single color data and the said color image data according to the said compositing

position information, identifying means for identifying a bit value of the said binary image data every one dot, and second selecting means for selecting any one of an output of the said first selecting means and the said second single color data in accordance with an identification result of the said identifying means.

Claim 7 (Currently Amended): An image display apparatus according to claim 1, wherein the said display circuitry means includes readout start position information fetching means for fetching readout start position information of the said-second image data in the second memory, and readout means for reading out the said-second image data from the said second memory according to the said readout start position information.

Claim 8 (Currently Amended): An image display apparatus according to claim 1, wherein the said display circuitry means displays an image based on the said-first image data in the first memory by priority.

Claim 9 (Currently Amended): A display control method executed by an image display apparatus provided with a first function which is being continuously set into an on state and a second function which is selectively being set into an on off state in case of necessity, the method comprising steps of:

- (a) writing first image data relating to the said first function to a first memory;
- (b) writing second image data relating to the said first function to a second memory when the said second function is in an off state;
- (c) writing second image data relating to the said second function to the said second memory when the said second function is in an on state; and
- (d) displaying a composite image on a display on the basis of the said-first image data stored in the said first memory and the said-second image data stored in the said second memory.

Claim 10 (Currently Amended): A display control program executed by an image display apparatus provided with a first function which is being continuously set into an on state and a second function which is selectively being set into an on off state ~~in case of necessity~~, the program comprising ~~steps of~~:

- (a) writing ~~first~~ image data relating to the said first function to a first memory;
- (b) writing ~~second~~ image data relating to the said first function to a second memory when the said second function is in an off state;
- (c) writing ~~second~~ image data relating to the said second function to the said second memory when the said second function is in an on state; and
- (d) displaying a composite image on a display on the basis of the said ~~first~~ image data stored in the said first memory and the said ~~second~~ image data stored in the said second memory.

Claim 11 (Currently Amended): A storage medium storing a display control program executed by an image display apparatus provided with a first function which is being continuously set into an on state and a second function which is selectively being set into an on off state ~~in case of necessity~~, the display control program[[,]] comprising ~~steps of~~:

- (a) writing ~~first~~ image data relating to the said first function to a first memory;
- (b) writing ~~second~~ image data relating to the said first function to a second memory when the said second function is in an off state;
- (c) writing ~~second~~ image data relating to the said second function to the said second memory when the said second function is in an on state; and
- (d) displaying a composite image on a display on the basis of the said ~~first~~ image data stored in the said first memory and the said ~~second~~ image data stored in the said second memory.

Claim 12 (New): An image display apparatus comprising one or more processing systems for executing the method of claim 9.

Claim 13 (New): An image display apparatus comprising the storage medium of claim 11.

Claim 14 (New):      An image display apparatus comprising:  
a first processor for executing a communication-related function;  
a second processor for executing a game-related function;  
a first memory for storing communication-function-related image data;  
a second memory for storing either communication-function-related image data or game-function-related image data; and  
a display controller for generating a display that comprises a non-composite display portion based on contents of one or the other of the first and second memories and a composite display portion based on a composite of contents of both the first and second memories.

Claim 15 (New):      The image display apparatus according to claim 14, wherein the second memory stores communication-function-related image data or game-function-related image data based on whether the game-related function is being executed by the second processor.

Claim 16 (New):      The image display apparatus according to claim 14, further comprising:  
an interrupt signal path between the first processor and the second processor.

Claim 17 (New):      The image display apparatus according to claim 14, further comprising:  
a common bus to which the first processor and the second processor are coupled.

Claim 18 (New):      The image display apparatus according to claim 14, further comprising:  
one or more registers,  
wherein locations and sizes of the non-composite and composite display portions are configurable via settings of the one or more registers.

Claim 19 (New):      The image display apparatus according to claim 14, wherein the communication-related function comprises a wireless communication-related function.

Claim 20 (New): The image display apparatus according to claim 14, wherein the first and second memories comprise respective frame memories.

Claim 21 (New): The image display apparatus according to claim 14, wherein the image data stored in the first memory comprises one-bit image data and the image data stored in the second memory comprises multi-bit image data.

Claim 22 (New): The image display apparatus according to claim 14, wherein, in response to a detecting of an incoming communication by the communication-related function, an instruction for pausing the game-related function is sent from the first processor to the second processor.

Claim 23 (New): The image display apparatus according to claim 14, wherein the display controller controls the tone of the contents of second memory based on whether or not the game-related function is being executed.

Claim 24 (New): The image display apparatus according to claim 14, embodied as a portable communication terminal.

Claim 25 (New): The image display apparatus according to claim 14, wherein the first processor continuously executes the communication-related function and the second processor selectively executes the game-related function.

Claim 26 (New): A hand-held image display apparatus comprising:  
processing circuitry for executing one or more functions;  
first and second memories each for storing image data; and  
a display controller for generating a display comprising a non-composite display portion based on contents of one or the other of the first and second memories and a composite display portion based on a composite of contents of both the first and second memories.

Claim 27 (New): The hand-held image display apparatus according to claim 26, wherein the one or more functions include a wireless communication-related function and a game-related function.

Claim 28 (New): The hand-held image display apparatus according to claim 26, wherein the image data stored in the first memory comprises one-bit image data and the image data stored in the second memory comprises multi-bit image data.

Claim 29 (New): The hand-held image display apparatus according to claim 26, embodied as a portable communication terminal.

Claim 30 (New): An image display method comprising:  
storing in a first memory image data relating to a first function of an image display apparatus;  
storing in a second memory image data relating to a second function when the second function of the image display apparatus is being executed;  
storing in the second memory image data relating to the first function when the second function is not being executed;  
displaying on a first portion of a display a non-composite image based on contents of one or the other of the first and second memories; and  
displaying on a second portion of the display a composite image based on a composite of contents of both the first and second memories.

Claim 31 (New): A storage device storing instructions executable by a processing system to perform the method according to claim 30.

Claim 32 (New): An image display apparatus comprising a storage device according to claim 31.

Claim 33 (New): An image display apparatus comprising:  
processing circuitry for executing first and second functions;

first and second memories; and

a display,

wherein, when the second function is in an off-state, binary image data and color image data for the first function are written to the first and second memories, respectively,

wherein, when the second function is in an on-state, color image data for the second function is written to the second memory instead of the color image data for the first function, and

wherein the display displays a composite image based on the binary image data stored in the first memory and the color image data stored in the second memory.

Claim 34 (New): The image display apparatus according to claim 33, wherein the first and second memories comprise respective frame memories.

Claim 35 (New): The image display apparatus according to claim 33, wherein the processing circuitry comprises respective first and second processors, the first processor executing the first function and the second processor executing the second function.

Claim 36 (New): The image display apparatus according to claim 33, wherein the display further displays a non-composite image based on the binary image stored in the first memory.

Claim 37 (New): The image display apparatus according to claim 33, wherein the first function is a communication-related function and the second function is a game-related function.

Claim 38 (New): The image display apparatus according to claim 33, embodied as a hand-held image display apparatus.

Claim 39 (New): The image display apparatus according to claim 33, embodied as a hand-held wireless communication apparatus.



Claim 40 (New):      An image display method comprising:  
storing binary image data relating to a first processing function in a first memory;  
selectively storing color image data relating either to the first processing function or to a  
second processing function in a second memory;  
generating a display comprising a composite display portion based on a composite of  
contents of both the first and second memories.

Claim 41 (New):      The image display method according to claim 40, wherein the  
generated display further comprises a non-composite display portion based on contents of one or  
the other of the first and second memories.

Claim 42 (New):      An image processing apparatus comprising processing circuitry for  
implementing the method according to claim 40.

Claim 43 (New):      The image processing apparatus according to claim 41, embodied  
as a hand-held image processing apparatus.

Claim 44 (New):      The image processing apparatus according to claim 41, embodied  
as a hand-held wireless communication apparatus.